

In the Claims

The following is a complete listing of the claims and replace all prior claims in the application:

- 1 1. (original) A data storage device, comprising:
2 media for storing data signals thereon;
3 an actuator for positioning a write head relative to the media;
4 a controller coupled to provide a control signal and the data signals, wherein the write
5 head comprises:
6 a data channel coupled to receive the control and data signals and coupled to
7 start a timer upon receipt of the control signal, wherein the data channel is coupled to
8 provide a write signal upon expiration of the timer; and
9 a write amplifier coupled to detect the write signal and coupled to provide a
10 flux inducing signal in response to the write signal upon detection of the write signal.
- 1 2. (original) The data storage device of claim 1, wherein the control signal starts
2 a warm-up period for the data channel and write amplifier.
- 1 3. (original) The data storage device of claim 2, wherein the warm-up period
2 expires when the write signal is provided.

1 4. (original) The data storage device of claim 3, wherein the write amplifier
2 comprises:
3 a detector coupled to receive the write signal and coupled to assert an enable signal
4 upon detection of the write signal; and
5 a final write stage coupled to receive the write signal and coupled to provide the flux
6 inducing signal after assertion of the enable signal.

7 5. (original) The data storage device of claim 4, wherein the final write stage
8 comprises:
9 a first transistor pair coupled to receive the write signal and coupled to provide a first
10 polarity of the flux inducing signal in response to a first level of the write signal;
11 a second transistor pair coupled to receive the write signal and coupled to provide a
12 second polarity of the flux inducing signal in response to a second level of the write signal;
13 and
14 a switch circuit coupled to prevent the flux inducing signal prior to the assertion of
15 the enable signal and coupled to allow the flux inducing signal after the assertion of the
16 enable signal.

1 6. (original) The data storage device of claim 5, wherein the first transistor pair
2 comprises N-type Field Effect Transistors.

1 7. (original) The data storage device of claim 5, wherein the second transistor
2 pair comprises N-type Field Effect Transistors.

1 8. (currently amended) A write channel of a magnetic head controller
2 comprising:
3 a data channel coupled to the write channel to receive write data and a control signal
4 and coupled to start a timer upon receipt of the control signal, wherein the data channel
5 transmits the write data upon expiration of the timer;
6 a detector coupled to the write channel to detect the write data and coupled to provide
7 a write control signal after detecting the write data; and
8 an amplifier coupled to the write channel to receive the write control signal
9 and the write data and coupled to convert the write data into a magnetic head signal in
10 response to the write control signal.

1 9. (original) The write channel of claim 8, wherein the detector comprises:
2 a logic circuit coupled to receive the write data and coupled to provide a detection
3 signal upon detection of a transition of the write data; and
4 a memory storage device coupled to receive the detection signal and coupled to
5 provide the write control signal.

1 10. (original) The write channel of claim 9, wherein the amplifier comprises:
2 a first transistor pair coupled to receive the write data and coupled to provide a first
3 polarity of the magnetic head signal in response to a first level of the write data;
4 a second transistor pair coupled to receive the write data and coupled to provide a
5 second polarity of the magnetic head signal in response to a second level of the write data;
6 and
7 a switch circuit coupled to prevent the flux inducing signal prior to the assertion of
8 the write control signal and coupled to allow the magnetic head signal after the assertion of
9 the write control signal.

1 11. (original) The write channel of claim 10, wherein the first transistor pair
2 comprises N-type Field Effect Transistors.

1 12. (original) The write channel of claim 10, wherein the second transistor pair
2 comprises N-type Field Effect Transistors.

1 13. (currently amended) A write channel of a magnetic head controller,
2 comprising:

3 a data channel means coupled to the write channel to receive write data and a control
4 signal and coupled to start a timer upon receipt of the control signal, wherein the data
5 channel means transmits the write data upon expiration of the timer;

6 a detector means coupled to the write channel to detect the write data and coupled to
7 provide a write control signal after detecting the write data; and

8 an amplifier means coupled to the write channel to receive the write control signal
9 and the write data and coupled to convert the write data into a magnetic head signal in
10 response to the write control signal.

1 14. (original) The write channel of claim 13, wherein the detector comprises:

2 a logic circuit coupled to receive the write data and coupled to provide a detection
3 signal upon detection of a transition of the write data; and

4 a memory storage device coupled to receive the detection signal and coupled to
5 provide the write control signal.

1

1 15. (original) The write channel of claim 14, wherein the amplifier comprises:
2 a first transistor pair coupled to receive the write data and coupled to provide a first
3 polarity of the magnetic head signal in response to a first level of the write data;
4 a second transistor pair coupled to receive the write data and coupled to provide a
5 second polarity of the magnetic head signal in response to a second level of the write data;
6 and
7 a switch circuit coupled to prevent the magnetic head signal prior to the assertion of
8 the write control signal and coupled to allow the flux inducing signal after the assertion of the
9 write control signal.

1 16. (original) The write channel of claim 15, wherein the first transistor pair
2 comprises N-type Field Effect Transistors.

1 17. (original) The write channel of claim 16, wherein the second transistor pair
2 comprises N-type Field Effect Transistors.

1 18. (original) A method of operating a write channel of a magnetic head
2 controller comprising:
3 establishing a write stabilization period while remaining in a read mode of operation;
4 transmitting write data when the stabilization period expires;
5 detecting the write data to cancel the read mode of operation; and
6 converting the write data to a magnetic head flux signal in response to detecting the
7 transmitted write data.

1 19. (original) The method of claim 18 wherein establishing the write stabilization
2 period comprises setting a configurable terminal count within a timer.

1 20. (original) The method of claim 18, wherein detecting the write data
2 comprises detecting a transition in the write data.

1 21. (original) The method of claim 18, wherein converting the write data
2 comprises: providing a first polarity of the magnetic head flux signal in response to a first
3 level of the write data; and
4 providing a second polarity of the magnetic head flux signal in response to a second
5 level of the write data.

1 22. (original) An article of manufacture comprising a program storage medium
2 readable by a computer, the medium tangibly embodying one or more programs of
3 instructions executable by the computer to perform a method of operating a write channel of
4 a magnetic head controller, the method comprising:
5 establishing a write stabilization period while remaining in a read mode of operation;
6 transmitting write data when the stabilization period expires;
7 detecting the write data to cancel the read mode of operation; and
8 converting the write data to a magnetic head flux signal in response to detecting the
9 transmitted write data.

1 23. (original) The method of claim 22 wherein establishing the write stabilization
2 period comprises setting a configurable terminal count within a timer.

1 24. (original) The method of claim 22, wherein detecting the write data
2 comprises detecting a transition in the write data.

1 25. (original) The method of claim 22, wherein converting the write data
2 comprises: providing a first polarity of the magnetic head flux signal in response to a first
3 level of the write data; and
4 providing a second polarity of the magnetic head flux signal in response to a second level of
5 the write data.